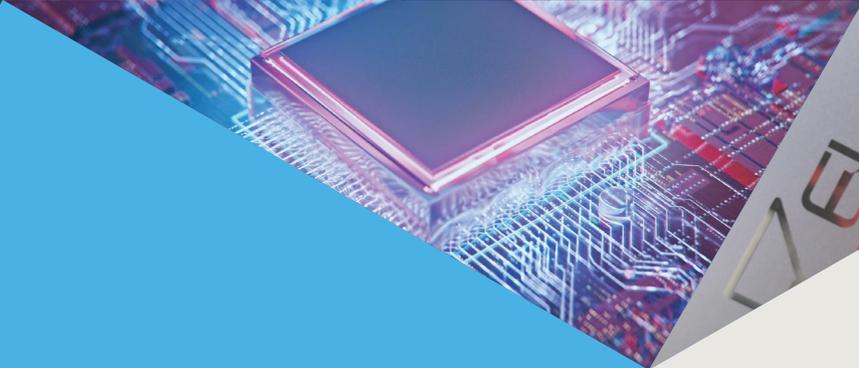




EIC ACADEMY
Engineer Incubation Center





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What is EIC Academy?

EIC Academy: “Engineer Incubation Center”

ElectraIC is also the leading company in the sector with the special applications it provides to its employees. The company provides newly graduated engineers with the opportunity to participate in the EIC Academy program developed by it.

For the recruitment process of newly graduated engineers at ElectraIC, EIC Academy is a professional Engineer Training Program issued in the fields of “Digital Design and Advanced Verification Techniques”.

As part of the EIC Academy, young engineers undergo an eight-week intensive program that includes “trainings in the field of Digital Design & VHDL and System Verilog & UVM”, “applied projects” and a detailed “assessment report” prepared by expert engineers in their fields. This allows young engineers to start their business life more equipped.



Engineers participating in the program carry out the following activities for eight weeks;

- 1) Getting trainings on different subjects in the field of electronic engineering,
- 2) Realizing real hands-on project practices,
- 3) Preparing a final presentation using the knowledge gathered during the trainings.



Content of the EIC Academy Program

The content of the program on a weekly basis is given as follows.

The program is a certified program.

The success level of the participants is assessed separately in the following fields:

- Technical Competence
- Personal Capabilities

DIGITAL DESIGN and VHDL

Week 1

Digital Design and VHDL Training

Week 2

Design with VHDL

Week 3

Verification with VHDL

Week 4

RTL Verification and FPGA Board Validation

SYSTEM VERILOG and UVM

Week 5

SystemVerilog Training

Week 6

UVM Training and Practice

Week 7

Verification using VIPs

Week 8

Documentation in Verification





Flow of EIC Academy Process

Candidate Application

- + Applications to ElectraIC

Pre-test

- + Pre-test by ElectraIC HR

Interview

- + HR Interview
- + Technical Interview

Election for Candidate for Nomination

- + Selection of candidates by scoring
- + Personality Inventory Report
- + English Level Test
- + Integrity Test
- + Attention, Intuition and Perception Test
- + Making offers to eligible candidates

»In line with the ElectraIC new graduate recruitment needs every year, EIC Academy is held twice a year, usually in the spring and autumn semesters.

»Participation in the EIC Academy Program is free of charge and the participants who complete the program are given a certificate of completion approved by ElectraIC and Doulos.

»The program consists of a minimum of six and a maximum of ten young engineers.

» Each participant is given a detailed feedback assessment issued by expert engineers in the fourth and eighth weeks of the program.

»The trainings under the EIC Academy are given by certified trainers. Project implementations are carried out under the consultancy of expert engineers. Assessments are conducted by groups of at least three expert engineers.

» Candidates are required to successfully pass their assessments in the fourth and eighth weeks in order to join the ElectraIC family by completing the EIC Academy Program.





Phase 1

- + 4-week Digital Design Techniques Training

Assessment of Phase-1

- + Assessment of the Phase-1 process

Phase 2

- + 4-week Advanced Verification Techniques Training

Assessment of Phase-2

- + Assessment of the Phase-2 process



Program Announcements

Program announcements are made 2 months before the start date from the company's social media channels, www.kariyer.net, www.vizyonergenc.com, and from the relevant channels of the universities. Candidates can submit their applications to channels such as kariyer.net or vizyonergenc.com or send them directly to careers@electraic.com.





Qualifications Required for Application to the Program

- + University Department: Electrical-Electronics Engineering, Electronics and Communication Engineering, Electronics Engineering, Microelectronics Engineering, Computer Engineering
- + Education Level: Undergraduate (Those who are in the 4th year and who will graduate by completing their internship with this program), Undergraduate (Graduate), Master (Graduate, Student), Doctorate (Graduate, Student)
- + Undergraduate and/or Master Degree
- + Generally, work experience is not required. Candidates are expected to have knowledge of basic logic circuit design. (Having knowledge and/or experience in ASIC/FPGA design and verification)
- + Linux and Windows knowledge
- + Verbal and written communication skills for documentation and reporting
- + Fluent in English
- + Military Status: Completed/exempted/postponed



The Advantages of the EIC Academy Program for Students and Young Engineer Candidates

- + Education certificates recognized abroad
- + Opportunity to receive training from internationally certified instructors
- + Opportunity to work with mentors who are experts in their fields
- + Opportunity to work on high-tech projects during the first two months of work life
- + Working environment with similarly experienced engineers



The program is supported by four trainings from the world-leading training services company Doulos. These trainings are as follows:

- + Essential Digital Design Techniques
- + VHDL for Designers
- + Comprehensive SystemVerilog
- + UVM Adopter Class

This very special program, enriched with the trainings of Doulos that also provides training to big companies such as ARM, Raytheon, MBDA, ST; has been organized in accordance with the needs of young engineer candidates.

For application, you can follow the announcements on our social media accounts and send your resume to careers@electraic.com.



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Design & Verification

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